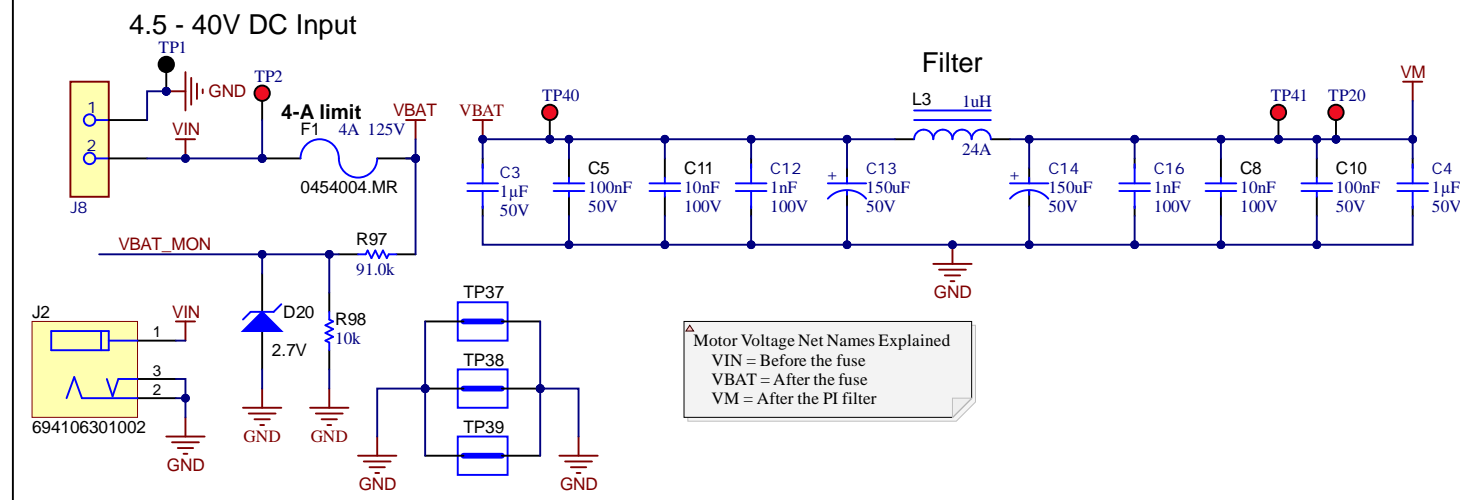
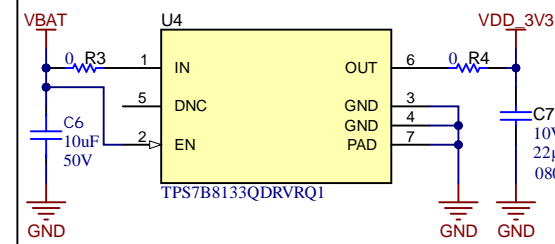


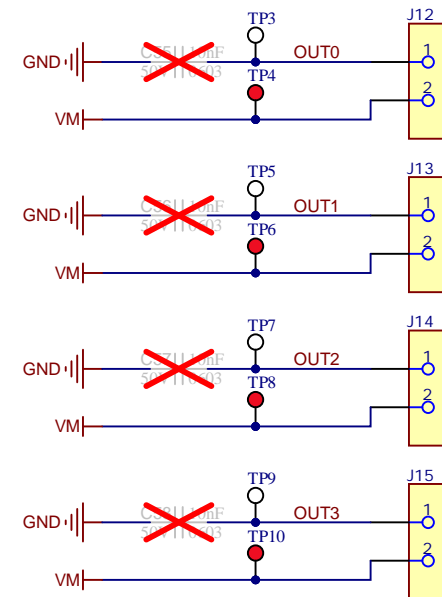
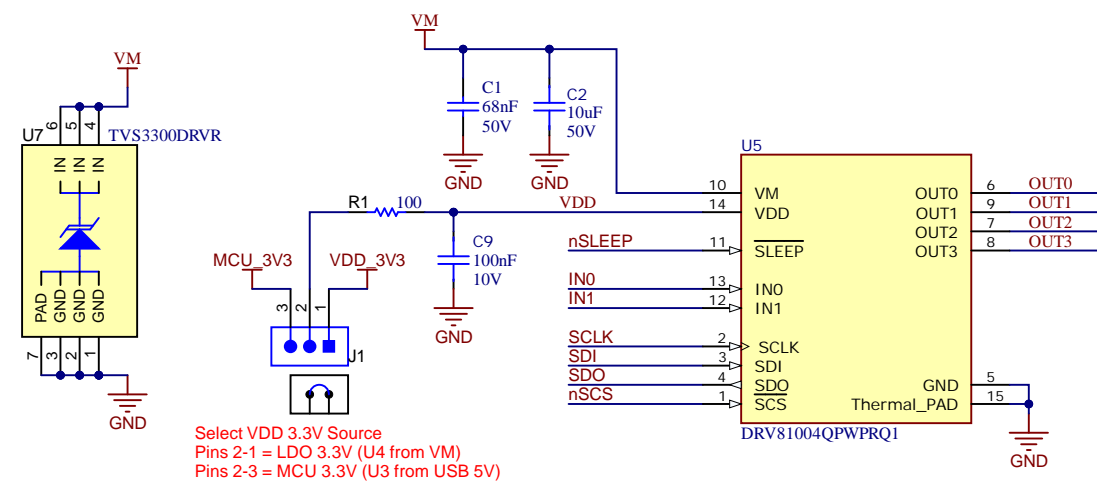
## Board power




### 3.3V LDO



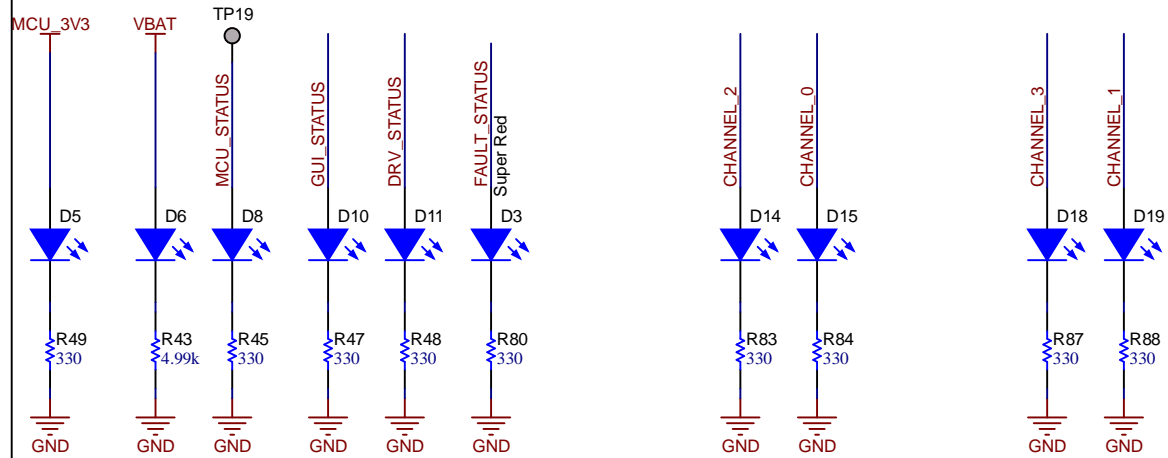
## Output Connectors

**DRV81004-Q1**

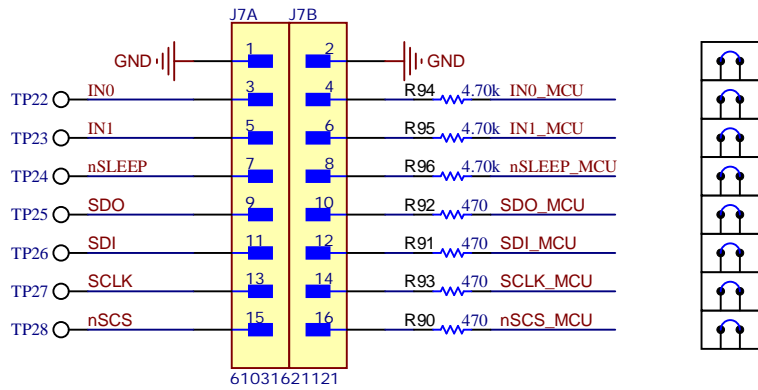
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: <b>DRV81004-Q1EVM</b>	Designed for: <b>Public Release</b>	Mod. Date: 7/22/2024	 <b>TEXAS INSTRUMENTS</b>  <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2024
TID #: <b>N/A</b>	Project Title: <b>MD086</b>		
Number: <b>MD086</b>	Rev: <b>B</b>	Sheet Title:	
SVN Rev: Unknown revision	Assembly Variant: <b>001</b>	Sheet: <b>1</b> of <b>4</b>	
Drawn By: <b>David Medis</b>	File: <b>MD086_DRV81004.SchDoc</b>	Size: B	
Engineer: <b>David Medis</b>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

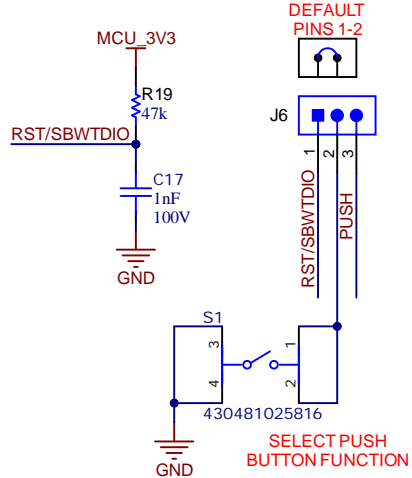
LEDS



Main Signal Header

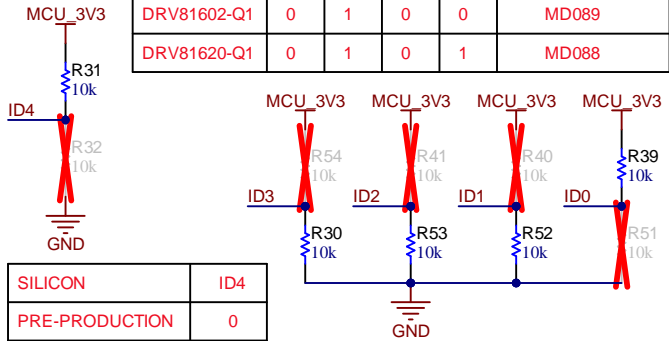


RST/PUSH Button



ID Resistors

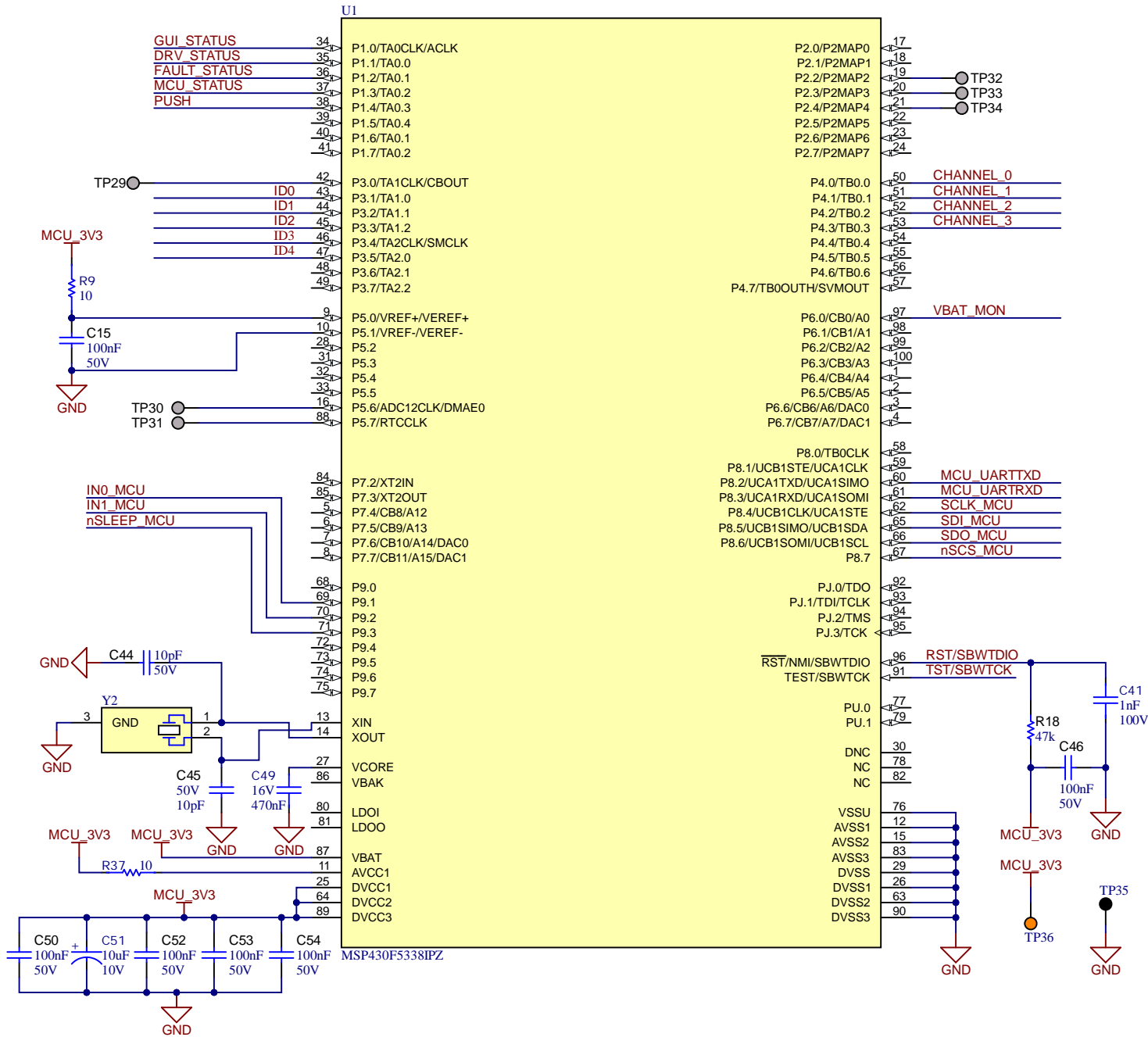
Device	ID3	ID2	ID1	ID0	MD #
DRV81008-Q1	0	0	0	0	MD075
DRV81004-Q1	0	0	0	1	MD086
DRV81080-Q1	0	0	1	0	MD081
DRV81242-Q1	0	0	1	1	MD091
DRV81602-Q1	0	1	0	0	MD089
DRV81620-Q1	0	1	0	1	MD088



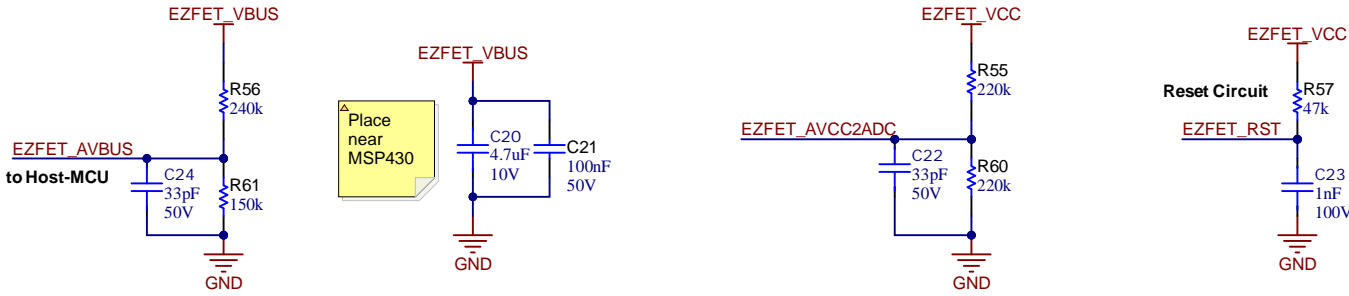
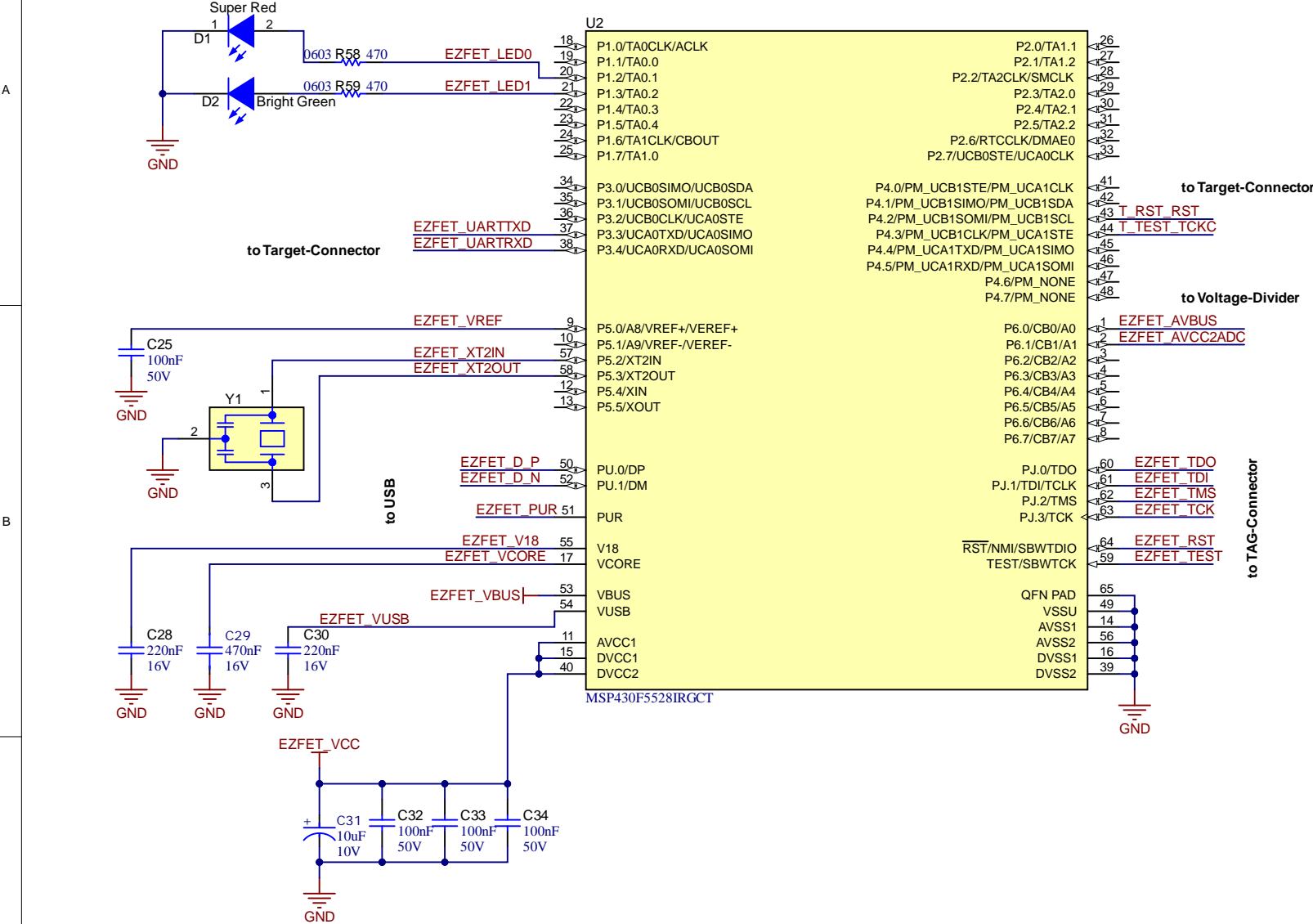
SILICON	ID4
PRE-PRODUCTION	0
PRODUCTION	1

The resistors on the ID[3:0] nets inform the firmware which device ID variant is on this board

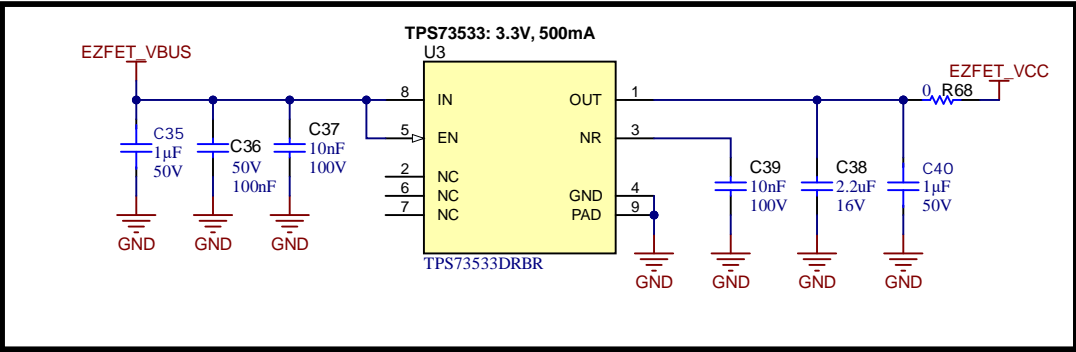
MSP430



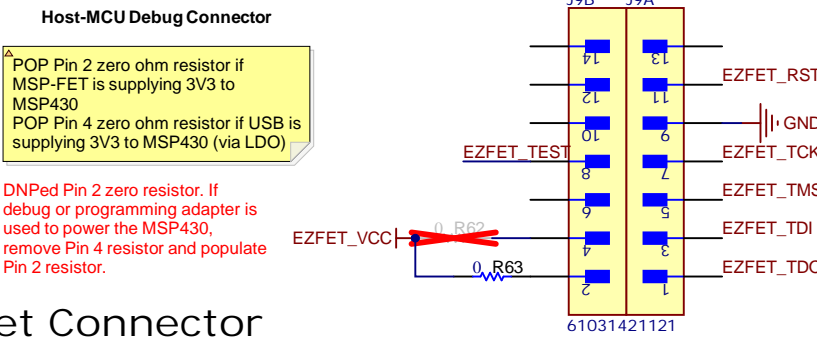
Host MCU for Emulation



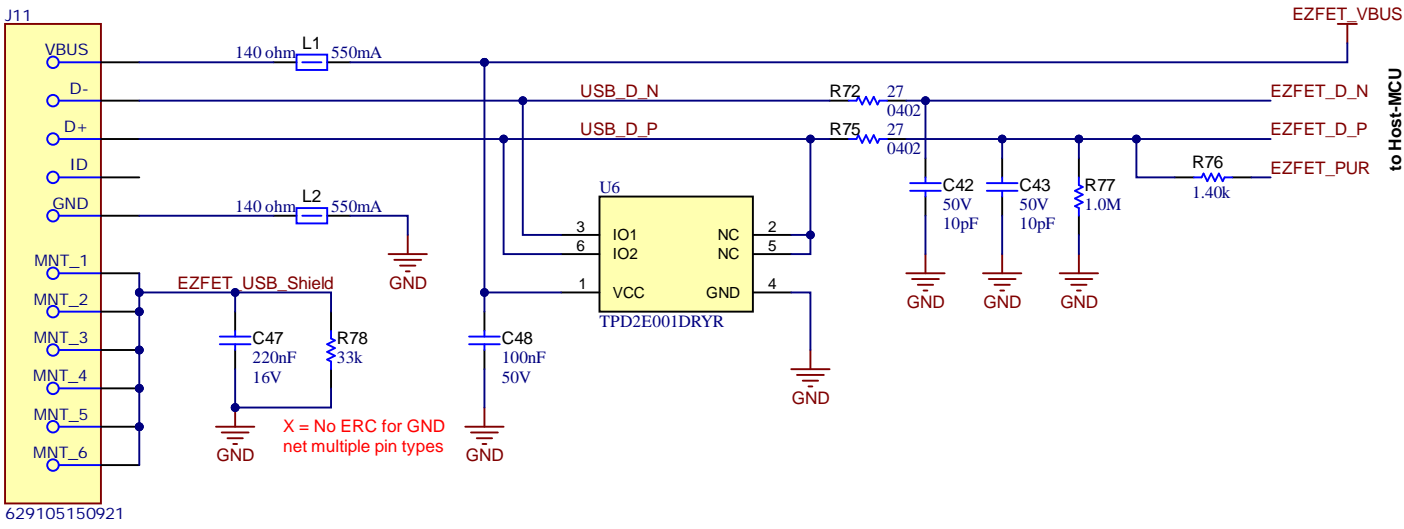
3.3V Power (EZFET\_VCC)



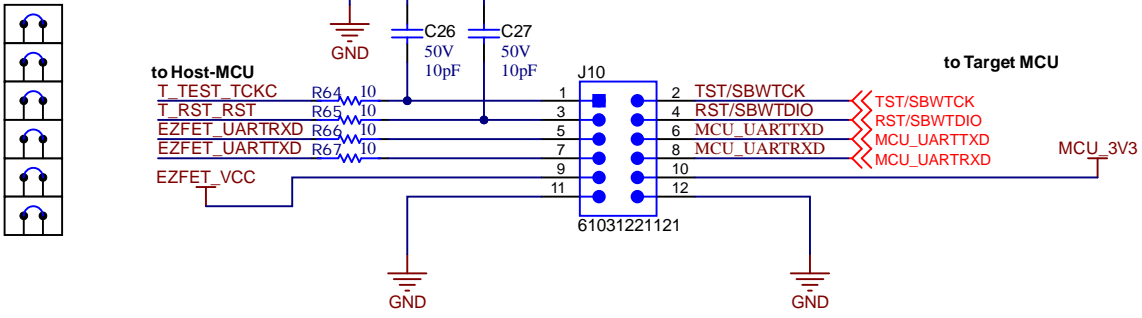
JTAG-Connector (Host Debug)



USB-I Interface



Target Connector



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